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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/771,773	02/04/2004	Jui-Yuan Tsai	251806-1030	4829
24504	7590	07/25/2005		EXAMINER
THOMAS, KAYDEN, HORSTEMEYER & RISLEY, LLP 100 GALLERIA PARKWAY, NW STE 1750 ATLANTA, GA 30339-5948				WELLS, KENNETH B
			ART UNIT	PAPER NUMBER
			2816	

DATE MAILED: 07/25/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/771,773	TSAI ET AL.
Examiner	Art Unit	
Kenneth B. Wells	2816	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Office Action Summary

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 07 June 2005.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-27 is/are pending in the application.
4a) Of the above claim(s) 15-27 is/are withdrawn from consideration.
5) Claim(s) _____ is/are allowed.
6) Claim(s) 1-14 is/are rejected.
7) Claim(s) _____ is/are objected to.
8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.

 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
4) Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
5) Notice of Informal Patent Application (PTO-152)
6) Other: _____

Art Unit: 2816

1. Applicant's election of Group II, filed on 6/7/05, is hereby acknowledged. Claims 1-14, as now amended, are seen to read on the elected embodiment. New claims 15-27 are hereby withdrawn from consideration, however, as being directed to a different invention. Applicant should note that claims 1-14 are directed to a subcombination (input buffer), whereas claim 15-27 are directed to a combination of the buffer with an A/D converter (claim 15 is an evidence claim).

2. Claims 1-14 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

It is misdescriptive, and thus indefinite, to recite that the switch module selects one of the transistor switches. As clearly shown in the instant drawings, it is some external (unillustrated) control circuitry that provides on/off signals to the switches SW3, SW4, etc in order to select one or more of the transistors Mp1, Mp2, etc.

As a minor point, in claim 10, "the transistor" on line 8 lacks antecedent basis.

Art Unit: 2816

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over applicant's admitted prior art (AAPA) Fig. 2 in view of Nicholson et al.

The AAPA Fig. 2 of the instant application shows an input buffer having all of the claimed features, except for the limitation of plural transistors in parallel, and the selection of one or more transistors in accordance with a code. Such would have been obvious, however, to those having ordinary skill in the art in view of Nicholson et al who teaches the use of parallel-connected FETs and selection switches which select one or more transistors in accordance with a code. The motivation for replacing applicant's elements R_s and M_p in instant Fig. 2 with the variable resistor arrangement of Nicholson et al is simply to allow variability of the resistor (it is old and well-known in the art that it is advantageous to have a variable resistor as opposed to a fixed resistor). Such would allow a

Art Unit: 2816

circuit designer to adjust the RC product "on the fly" without having to replace the resistor and FET of instant Fig. 2.

As to the limitation that the input buffer is within an AFE device of an LCD controller, and the A/D converter, it is noted that this is also admitted in applicant's background discussion as already known in the art as well.

As to the recitation of a thermometer code, this is also taught by Nicholson et al as a known means for selecting the one or more transistors (see Fig. 3).

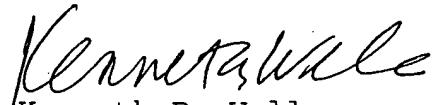
4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kenneth B. Wells whose telephone number is (571)272-1757. The examiner can normally be reached on Monday through Friday from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan, can be reached at (571)272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2816

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Kenneth B. Wells
Primary Examiner
Art Unit 2816

July 22, 2005